

23 Lecture - CS302

Important Mcqs

1. **What is the application of S-R latch?**

- A. Memory element
- B. Frequency modulation
- C. Power amplification
- D. Voltage regulation

Answer: A

What are the two inputs of an S-R latch?

- A. A and B
- B. C and D
- C. S and T
- D. S and R

Answer: D

What are the two outputs of an S-R latch?

- A. P and Q
- B. Q and R
- C. Q and Q'
- D. R and S

Answer: C

In which application is S-R latch used as a flip-flop?

- A. Memory
- B. Amplification
- C. Modulation
- D. Demodulation

Answer: A

What is the function of S input in S-R latch?

- A. Set the output to high
- B. Set the output to low
- C. Reset the output to high
- D. Reset the output to low

Answer: A

What is the function of R input in S-R latch?

- A. Set the output to high
- B. Set the output to low
- C. Reset the output to high
- D. Reset the output to low

Answer: C

Which logic gate is used to implement an S-R latch?

- A. AND gate

- B. OR gate
- C. NOT gate
- D. XOR gate

Answer: B

In which application is S-R latch used for pulse shaping?

- A. Signal conditioning
- B. Data synchronization
- C. Frequency modulation
- D. Power amplification

Answer: A

What is the output of an S-R latch when both S and R inputs are high?

- A. Q and Q? are both high
- B. Q is high and Q? is low
- C. Q is low and Q? is high
- D. Q and Q? are both low

Answer: D

What is the output of an S-R latch when both S and R inputs are low?

- A. Q and Q? are both high
- B. Q is high and Q? is low
- C. Q is low and Q? is high
- D. Q and Q? are both low

Answer: The output is indeterminate in this case as it depends on the previous state of the latch.