

17 Lecture - CS401

Important Mcqs

1. **What is the data bus width of the Motorola 68K processor?**

- A) 8 bits
- B) 16 bits
- C) 32 bits
- D) 64 bits

Answer: C) 32 bits

What was the maximum clock speed of the Motorola 68000 processor?

- A) 2 MHz
- B) 8 MHz
- C) 16 MHz
- D) 33 MHz

Answer: C) 16 MHz

Which addressing mode of the 68K processor allows for the addressing of data using a displacement relative to the program counter?

- A) Register addressing
- B) Immediate addressing
- C) Relative addressing
- D) Absolute addressing

Answer: C) Relative addressing

Which version of the 68K processor introduced support for virtual memory?

- A) 68000
- B) 68010
- C) 68020
- D) 68030

Answer: C) 68020

Which of the following is not a register in the 68K processor?

- A) A0
- B) D7
- C) PC
- D) SP

Answer: B) D7

What is the maximum amount of memory that can be addressed by the 68K processor?

- A) 1 MB
- B) 4 GB
- C) 16 MB
- D) 4 TB

Answer: B) 4 GB

Which instruction set architecture is used by the 68K processor?

- A) CISC

- B) RISC
- C) VLIW
- D) EPIC

Answer: A) CISC

Which of the following is not a member of the 68K processor family?

- A) 68000
- B) 68010
- C) 68030
- D) 68060

Answer: D) 68060

Which interrupt handling method is used by the 68K processor?

- A) Polling
- B) Daisy chaining
- C) Vectored interrupt
- D) Direct memory access

Answer: C) Vectored interrupt

Which operating system was not supported by the Motorola 68K processor?

- A) Mac OS
- B) Windows
- C) AmigaOS
- D) Unix

Answer: B) Windows