

4 Lecture - CS501

Important Mcqs

1. What does ISA stand for?

- a) Integrated Software Architecture
- b) Instruction Set Architecture
- c) Interface System Architecture
- d) Interconnect System Architecture

Answer: b) Instruction Set Architecture

What is the purpose of ISA?

- a) To define the functionality and operation of a processor
- b) To define the architecture of a computer system
- c) To define the programming language used by a processor
- d) To define the operating system used by a computer

Answer: a) To define the functionality and operation of a processor

What are instruction formats?

- a) Binary code used to represent instructions in machine language
- b) Programming tools used to write instructions for a processor
- c) The architecture of a computer system
- d) The operating system used by a computer

Answer: a) Binary code used to represent instructions in machine language

What is an opcode?

- a) A type of instruction format
- b) A binary code used to represent a specific operation
- c) A programming tool used to write instructions
- d) The architecture of a computer system

Answer: b) A binary code used to represent a specific operation

Which of the following is an example of an operand?

- a) ADD
- b) 0x04
- c) MOV
- d) JMP

Answer: b) 0x04

What does RISC stand for?

- a) Reduced Instruction Set Computer
- b) Random Instruction Set Computer
- c) Relocatable Instruction Set Computer
- d) Robust Instruction Set Computer

Answer: a) Reduced Instruction Set Computer

Which of the following is an advantage of RISC architecture?

- a) It has a large number of complex instructions

- b) It can perform complex instructions in a single clock cycle
- c) It is more power-efficient than CISC architecture
- d) It is more versatile than CISC architecture

Answer: c) It is more power-efficient than CISC architecture

Which of the following is a characteristic of CISC architecture?

- a) It has a small number of simple instructions
- b) It can perform complex instructions in a single clock cycle
- c) It is more power-efficient than RISC architecture
- d) It is more versatile than RISC architecture

Answer: b) It can perform complex instructions in a single clock cycle

Which instruction format specifies the location of an operand in memory?

- a) Register addressing
- b) Immediate addressing
- c) Direct addressing
- d) Indirect addressing

Answer: c) Direct addressing

Which instruction format specifies an operand that is contained in the instruction itself?

- a) Register addressing
- b) Immediate addressing
- c) Direct addressing
- d) Indirect addressing

Answer: b) Immediate addressing