10 Lecture - CS501

Important Mcqs

- 1. Which of the following is a custom design instruction set architecture developed by Qualcomm?
 - a) ISA
 - b) FALCON-E
 - c) ARM
 - d) x86

Answer: b) FALCON-E

What is the aim of FALCON-E architecture?

- a) To improve performance and energy efficiency for specific applications
- b) To provide compatibility across different processors
- c) To simplify the instruction set
- d) None of the above

Answer: a) To improve improve performance and energy efficiency for specific applications

Which of the following is a standardized instruction set architecture used by many processor manufacturers?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: c) ISA

Which architecture provides compatibility across different processors?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: c) ISA

Which of the following is true regarding FALCON-E architecture?

- a) It aims to simplify the instruction set
- b) It is a standardized instruction set architecture
- c) It provides compatibility across different processors
- d) It is developed by Intel

Answer: a) It aims to simplify the instruction set

What are the factors that need to be analyzed while comparing FALCON-E and ISA?

- a) Instruction set complexity
- b) Power consumption
- c) Compatibility with software
- d) All of the above

Answer: d) All of the above

Which architecture provides improved performance and energy efficiency for specific

applications?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: a) FALCON-E

Which architecture is used by many processor manufacturers to ensure compatibility?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: c) ISA

Which architecture is most suitable for applications that require high performance and energy efficiency?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: a) FALCON-E

Which architecture is most suitable for applications that require compatibility across different processors?

- a) FALCON-E
- b) ARM
- c) ISA
- d) x86

Answer: c) ISA